

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Masaki ISHIDAO et al.**

Serial Number: **Not Yet Assigned**

Filed: **February 2, 2004**

For: **SEMICONDUCTOR MEMORY DEVICE AND MANUFACTURING METHOD  
THEREOF**

Attorney Docket No. **032209**  
Customer No.: **38834**

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

February 2, 2004

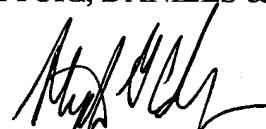
Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

A copy of the reference is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge  
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Respectfully submitted,  
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Enclosures: PTO-1449; Reference (1)

**INFORMATION  
DISCLOSURE  
STATEMENT  
PTO-1449**

Atty. Docket No. 032209 Serial No. New Appln.

Applicant(s): Masaki ISHIDAO et al.

Filing Date: February 2, 2004 Group Art Unit:

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
	AA					
	AB					
	AC					
	AD					

**FOREIGN PATENT DOCUMENTS**

	Document No.	Date	Country	Translation (Yes or No)
	AE			
	AF			
	AG			
	AH			
	AI			

**OTHER DOCUMENTS**

	AJ	Patent Abstracts of Japan, Publication No. 2001168219, dated June 22, 2001.
	AK	

Examiner

Date Considered